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				Application Number	10/696,502	
			CLOSURE	Filing Date	October 29, 2003	
STA	TEMENT E	BY A	PPLICANT	First Named Inventor	John S. Csapo	
	(Use as many sho	n sa stan	acassan/l	Art Unit	2681	
			·	Examiner Name	Not Yet Assigned	
Sheet	1	of	1	Attorney Docket Number	2003.10.004.WS0	

Examiner	Cite	NON PATENT LITERATURE DOCUMENTS	,		
Examiner Initials*	No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T²		
msc	AA	Juan Ignacio Osa et al., "MOSFET-C Filter With On-Chip Tuning And Wide Programming Range", IEEE Transactions on Circuits and Systems-II: Analog and Digital Signal Processing, Vol. 48, No. 10, October 2001.	_		
Msc	АВ	Jose Silva-Martinez et al., "A 10.7-MHz 68-dB SNR CMOS Continuous-Time Filter With On-Chip Automatic Tuning", IEEE Journal of Solid-State Circuits, Vol. 27, No. 12, December 1992.			
msc	AC	Aydin liker Karsilayan, "A Novel Automatic Tuning Scheme For High-Frequency High-Q Continuous-Time Filters", Texas A&M University Department of Electrical Engineering, IEEE 2001.)		

Examiner Signature Manuelisse Santiago - Cordero Considered 10/5/05					
Signature /// Considered 10/5/15	Examiner		1 1 2	Date	, /
- J- 1 - Was	Signature	Monuelisse	Santiago - Cordero	Considered	10/5/05

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